



Attorney's Docket No.: 09464-029001

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Applicant : You et al.
 Serial No. : 10/796,405
 Filed : March 8, 2004

Art Unit : 2816
 Examiner : My Trang Ton
 Confirmation No.: 9025
 Notice of Allowance Date: February 25, 2005

Title : METHOD AND APPARATUS FOR OPERATING A FIELD-EFFECT
 TRANSISTOR (FET) PAIR

MAIL STOP ISSUE FEE
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet are Comments on Statement of Reasons for Allowance and Issue Fee Transmittal PTOL 85(b), faxed this 3rd day of May, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,

Kelvin M. Vivian
 Reg. No. 53,727

Date: May 3, 2005

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COMMENTS ON STATEMENTS OF REASONS FOR ALLOWANCE

Applicant does not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. In particular, Applicant does not concede that the identified limitations – i.e., a control unit having a first control signal coupled to the first gate and a second control signal coupled to the second gate, in which the first control signal and the second control signal are separate control signals, and the first control signal has a rising edge with a different timing than a rising edge of the second control signal, in combination with the first FET and the second FET (claim 1), or the combination of all the method steps recited in claim 23 – are the only grounds for patentability of the allowed claims. Furthermore, the claims may be patentable for other reasons. In addition, Applicant notes that the dependent claims may also be allowable on their own merits.

Date: 05-03-05

Respectfully submitted,


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